

Figure 1

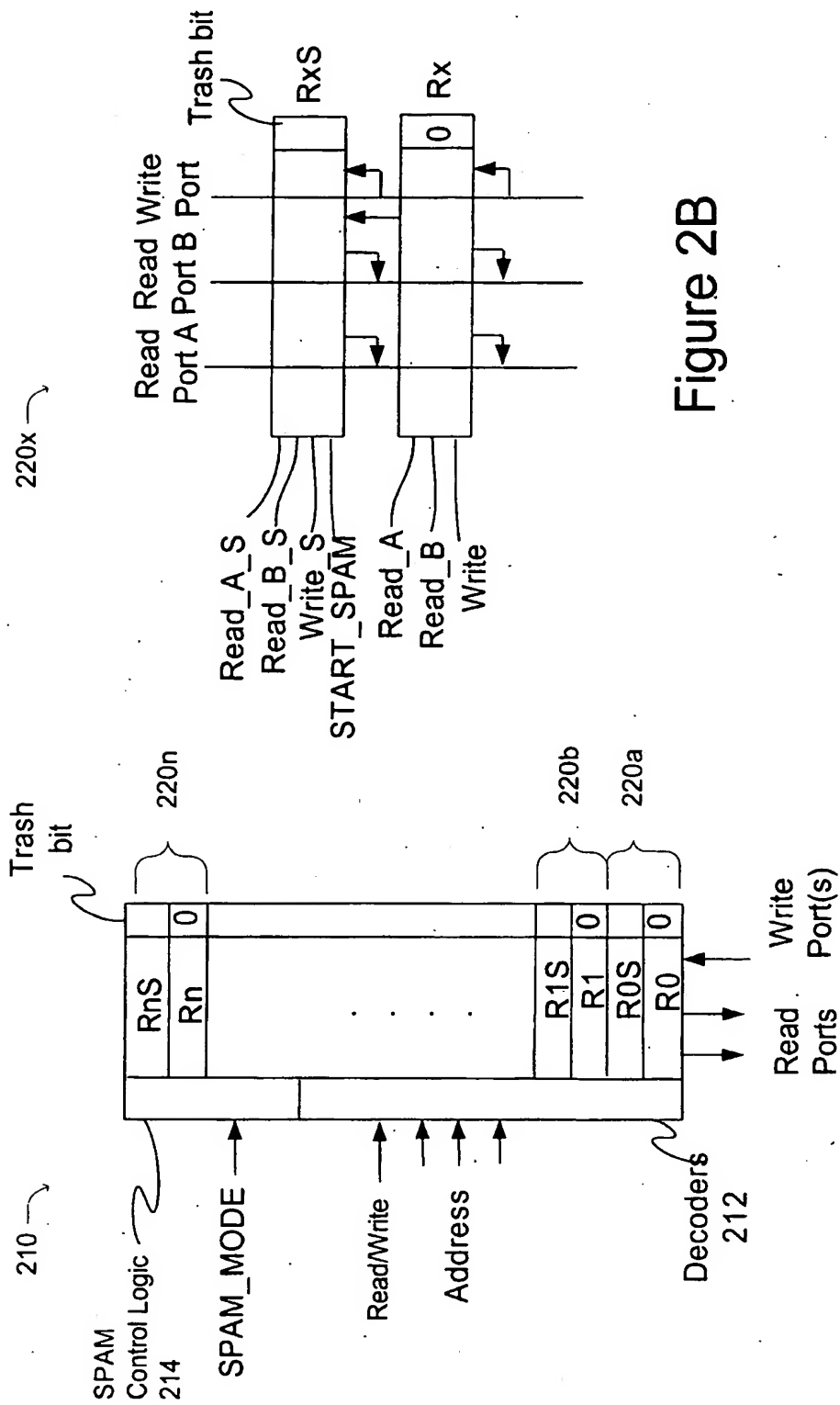


Figure 2A

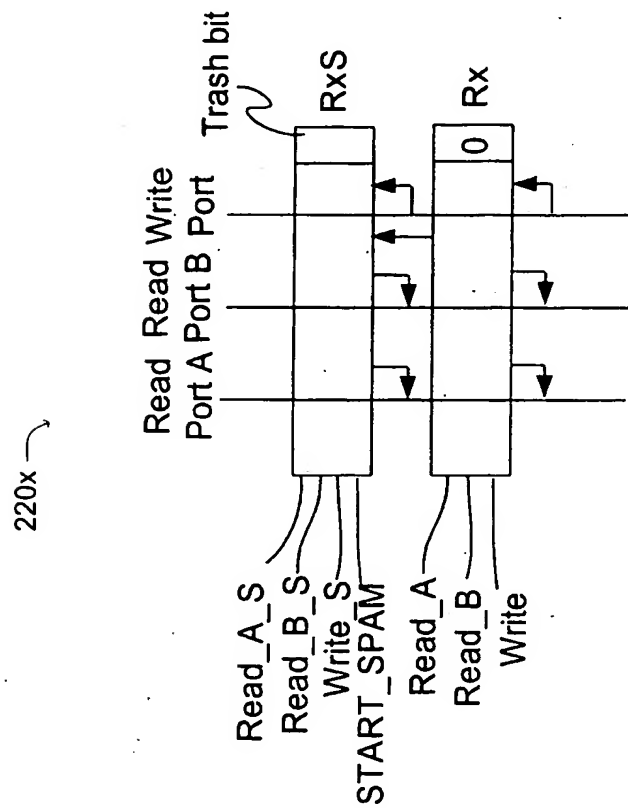


Figure 2B

300

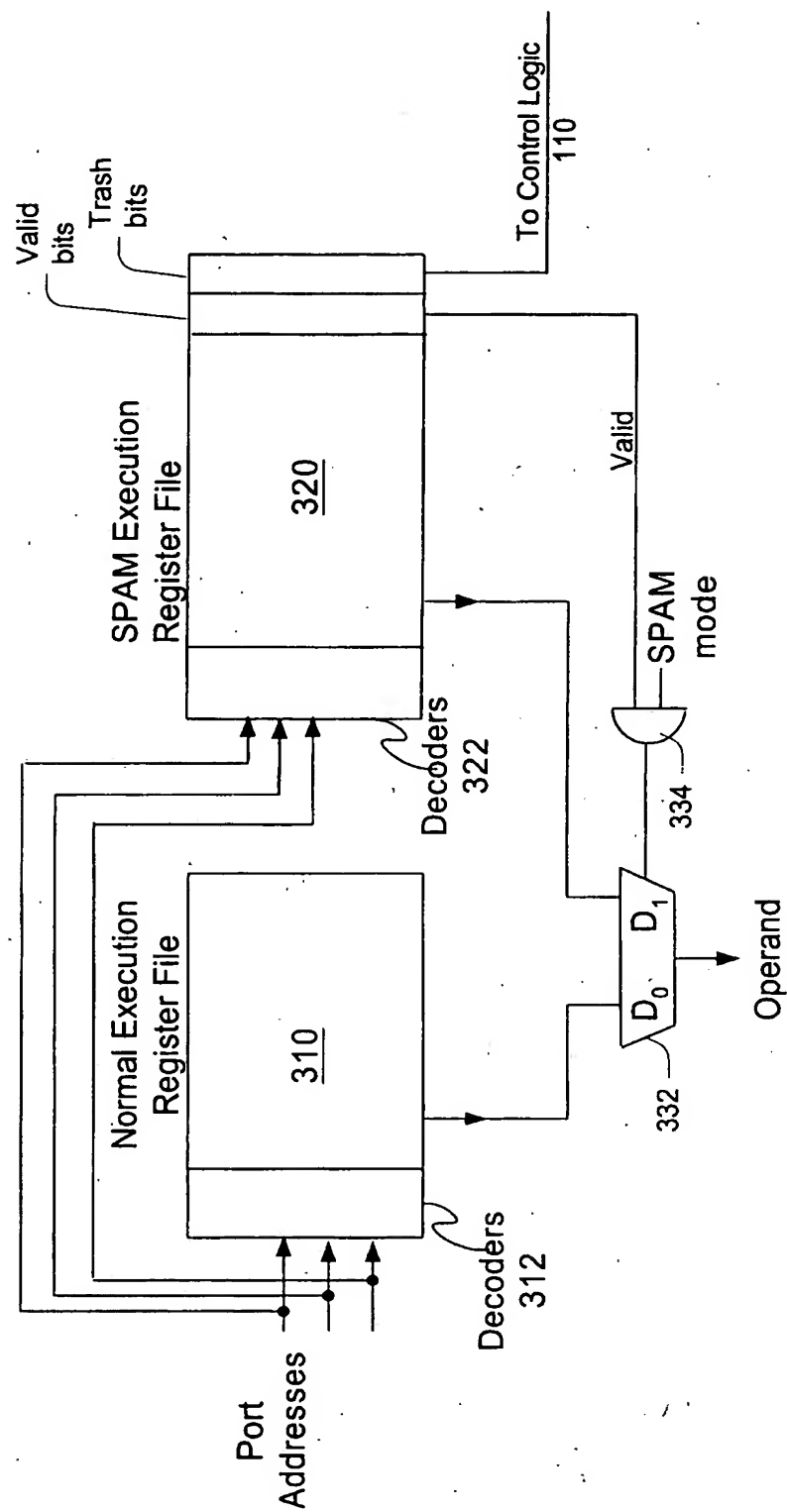


Figure 3A

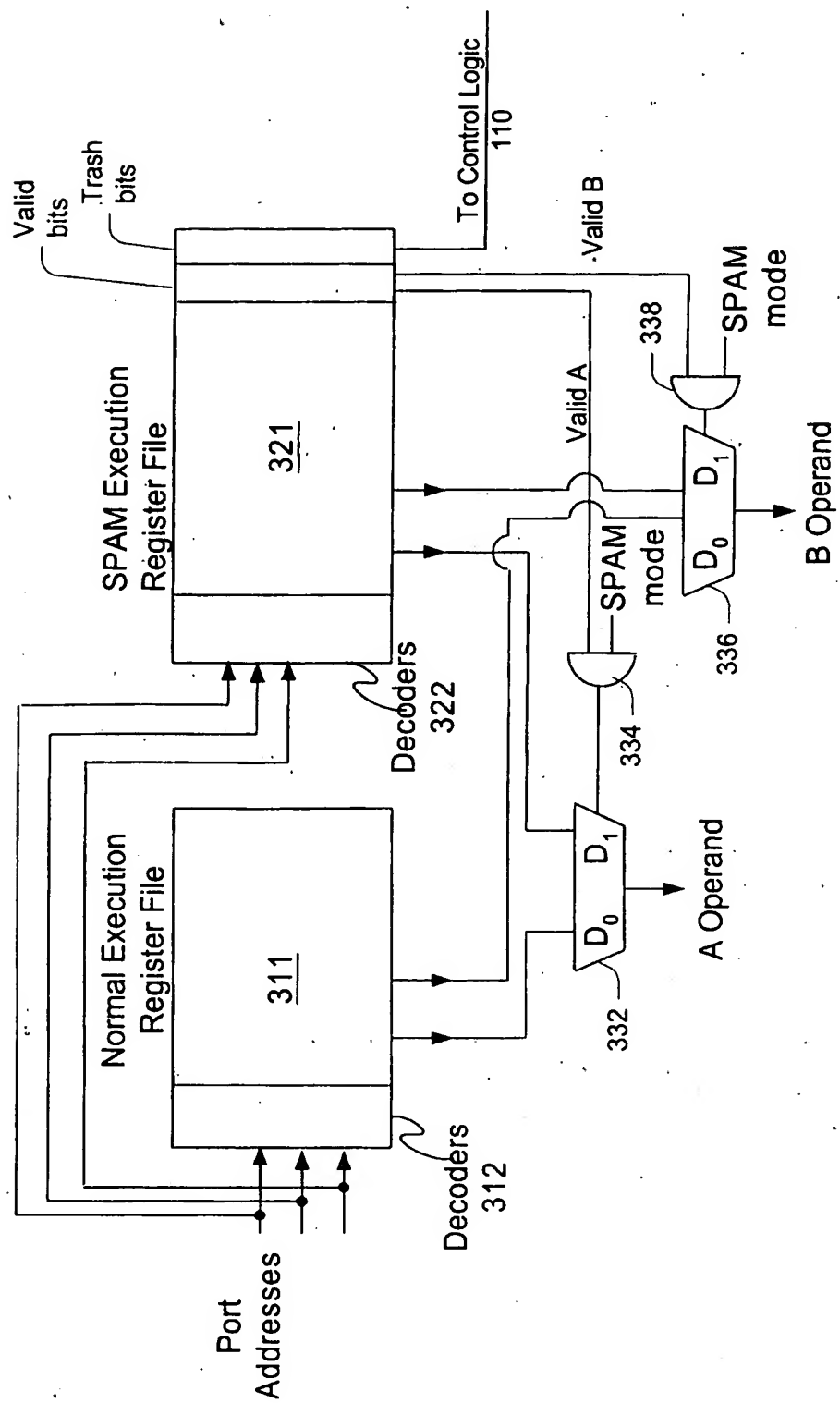


Figure 3B

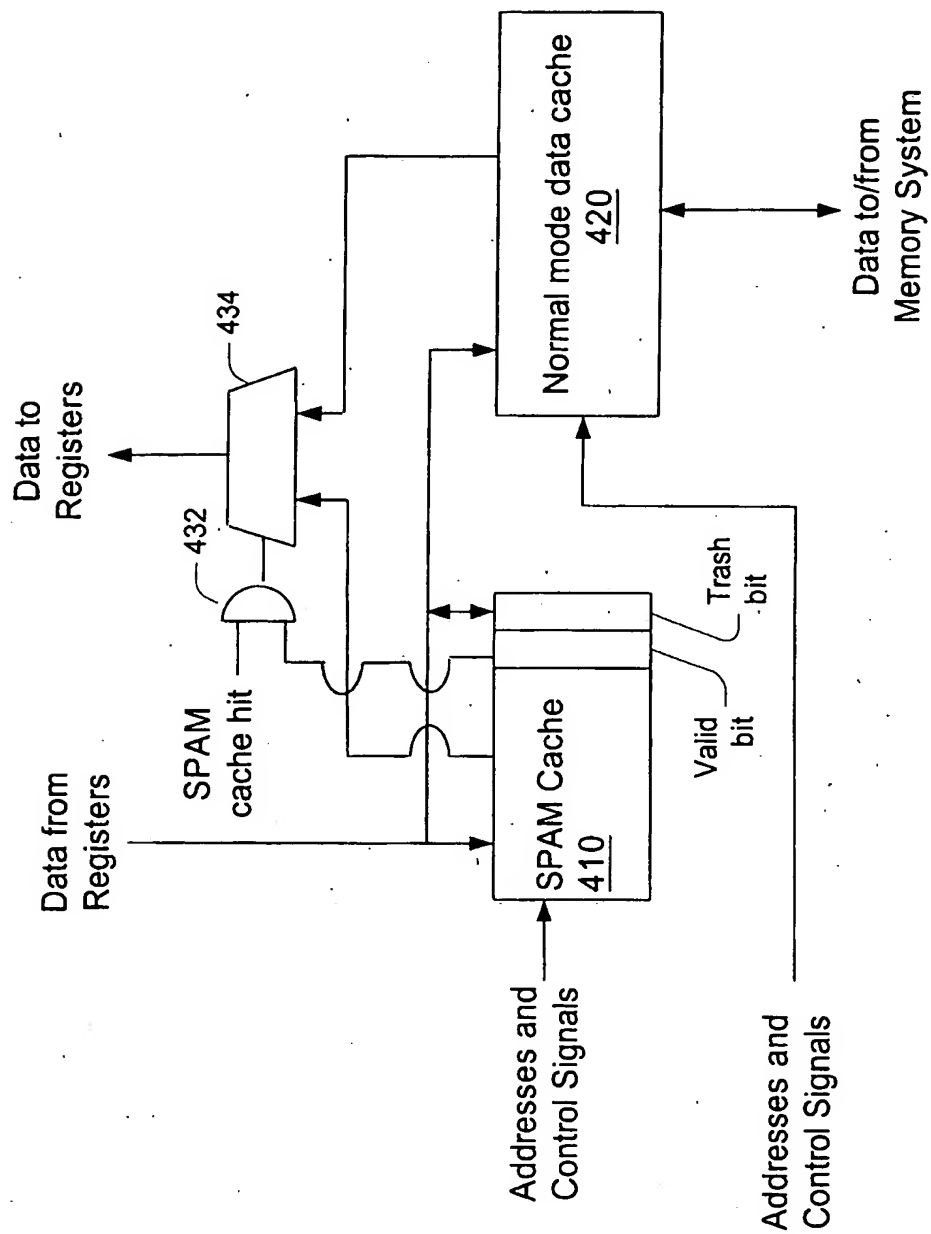


Figure 4

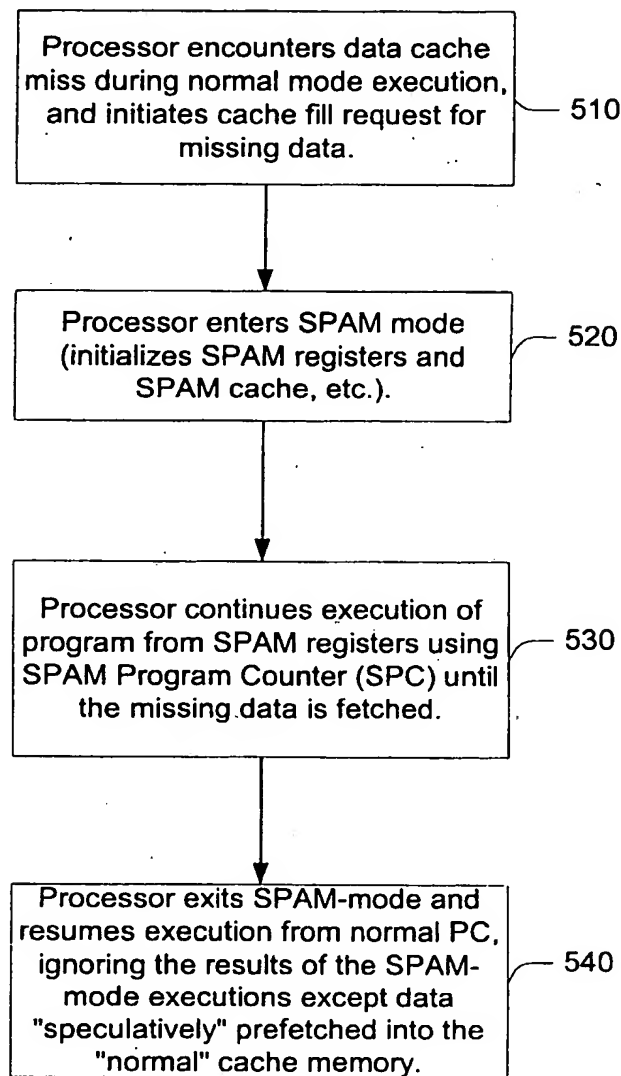
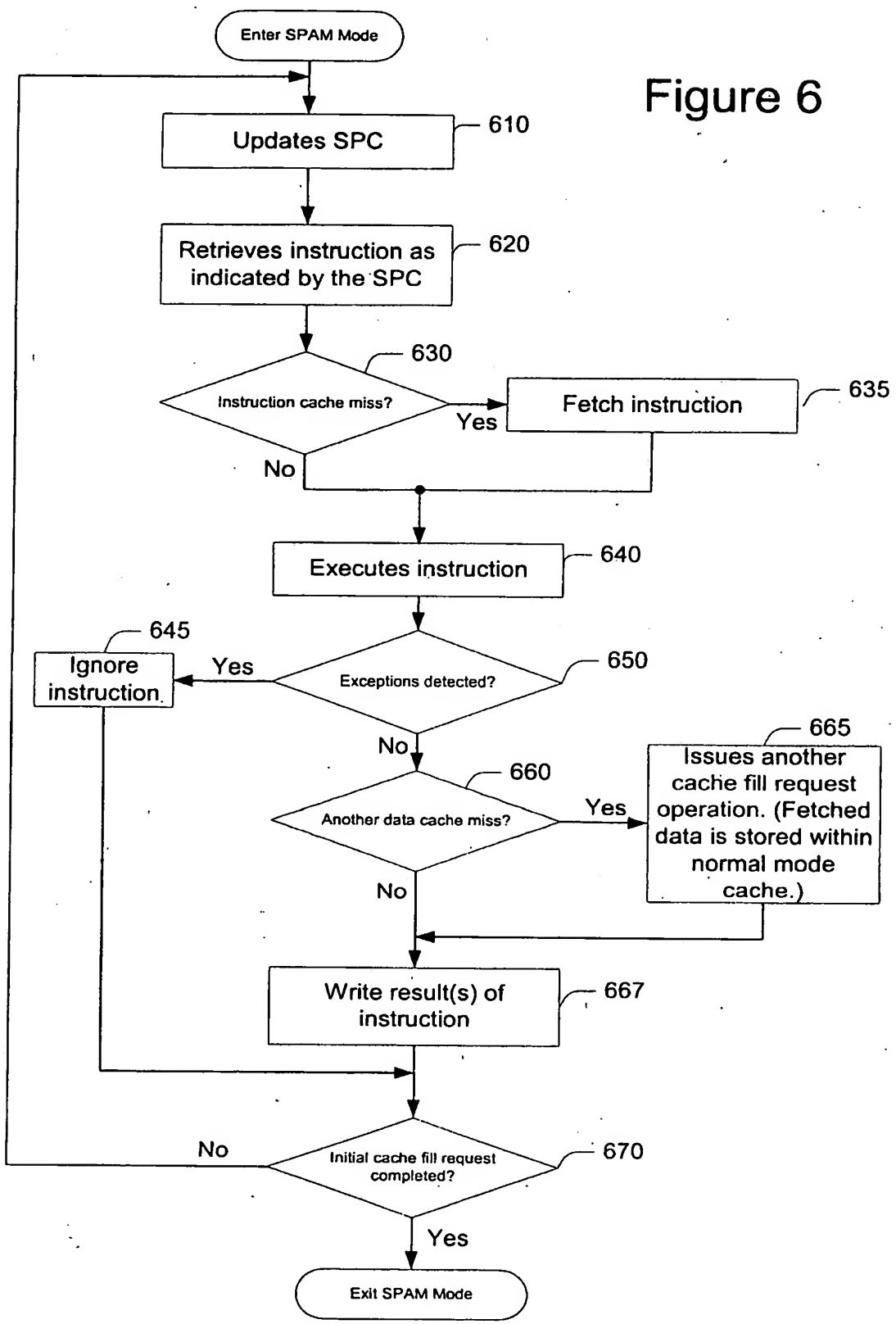


Figure 5

Figure 6



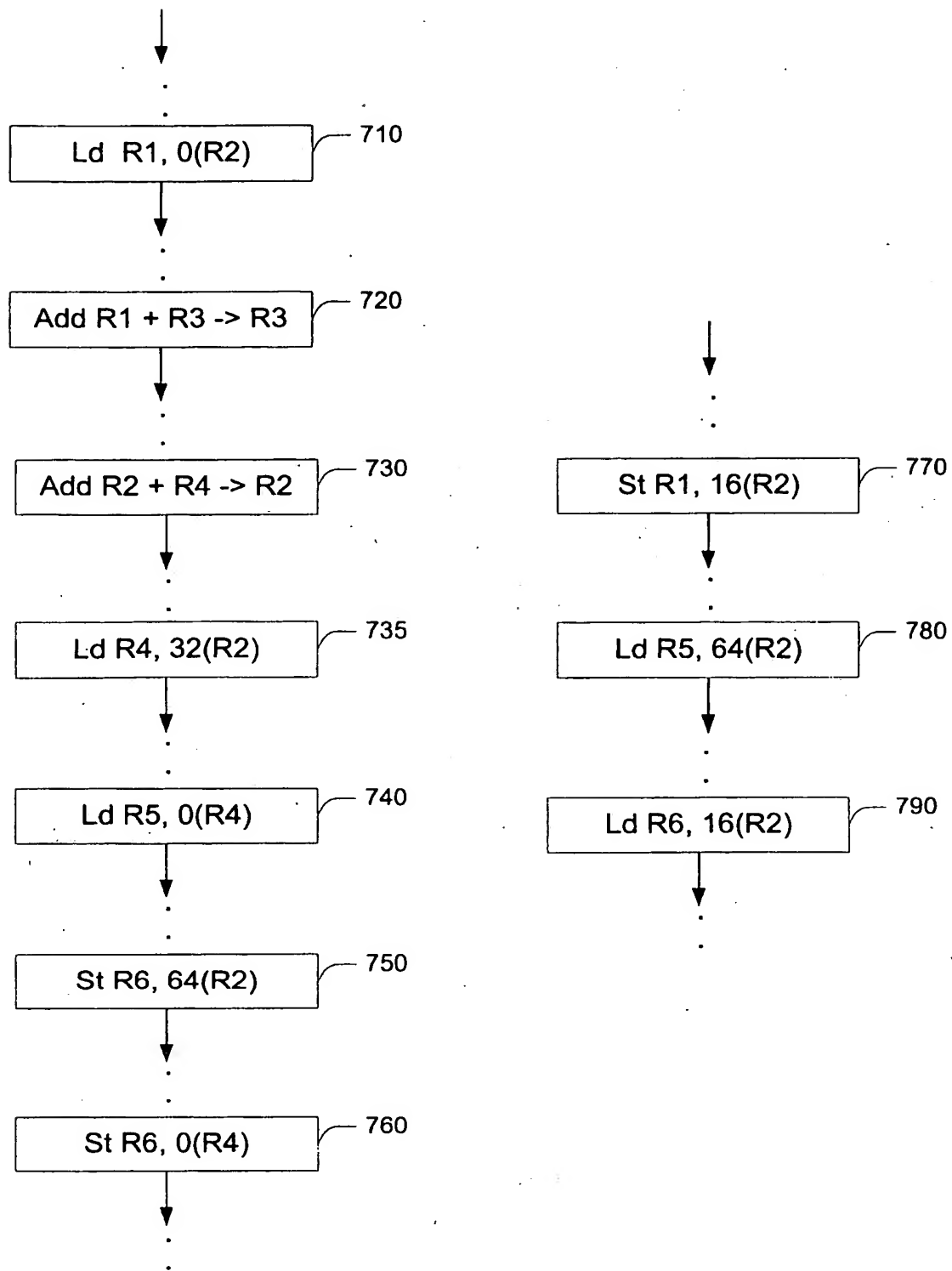


Figure 7

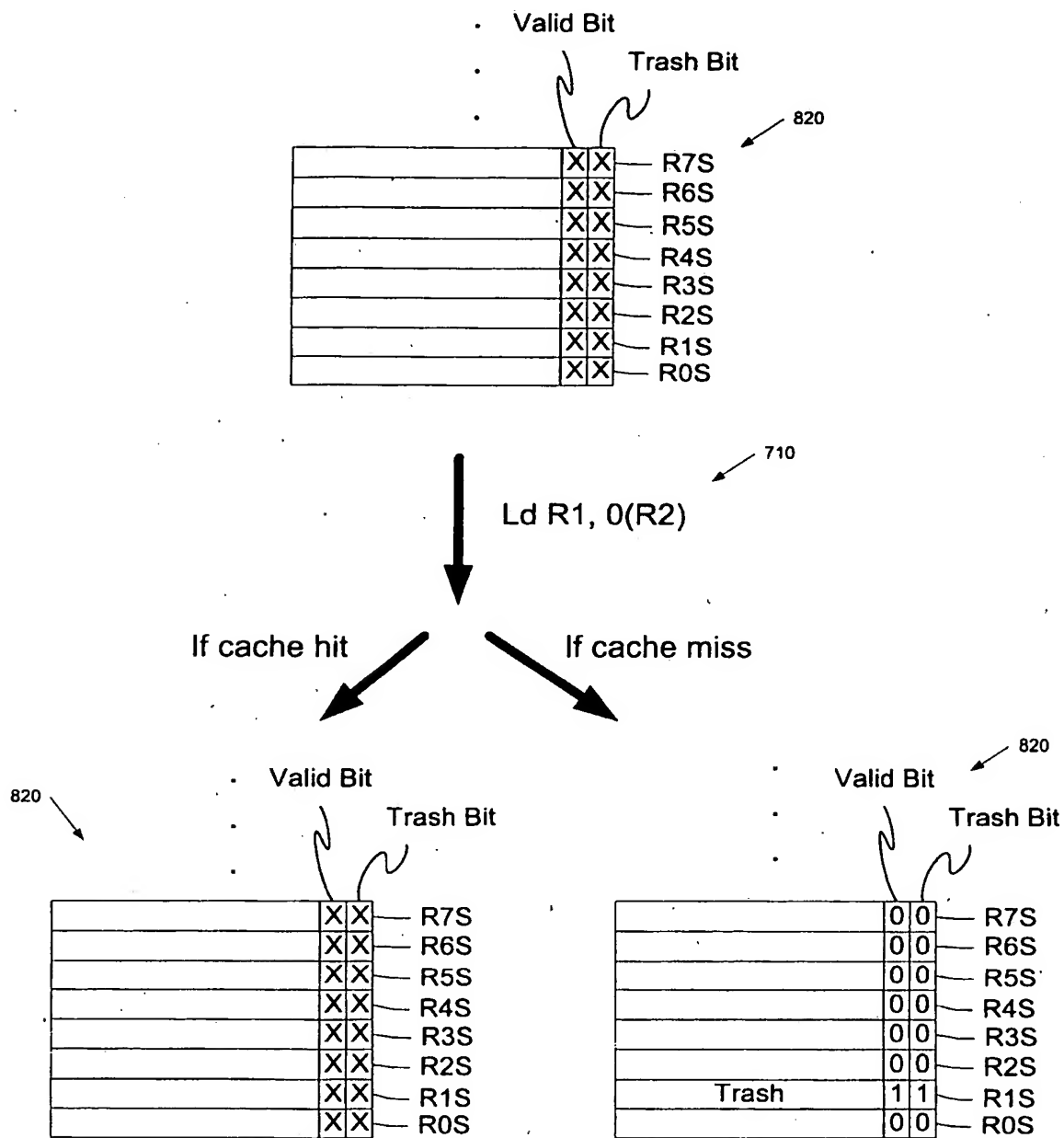
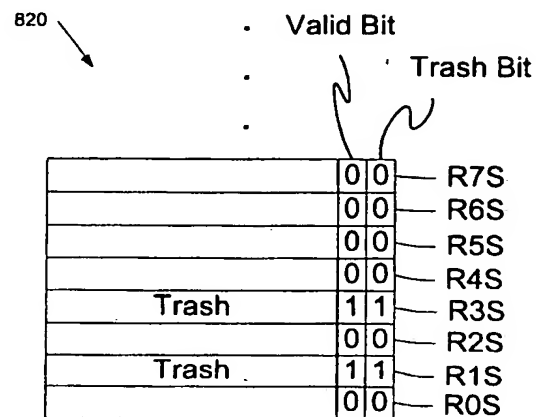
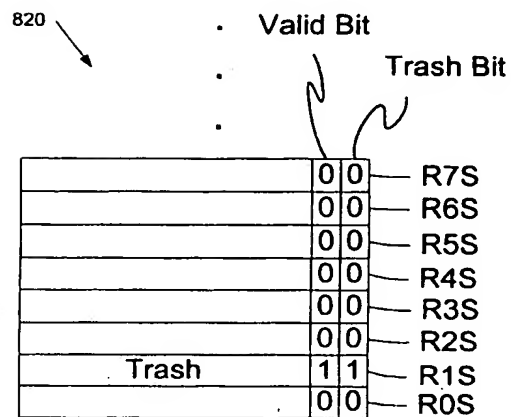


Figure 8



720

Add R1 + R3 -> R3

730

Add R2 + R4 -> R2

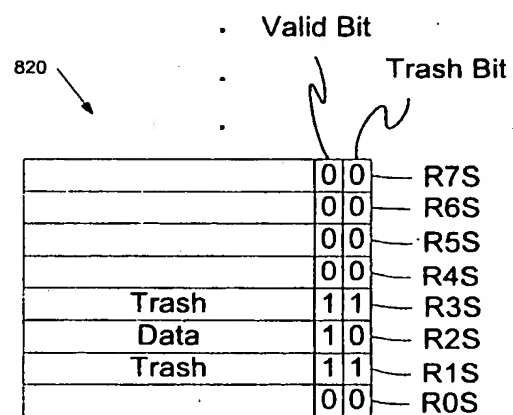
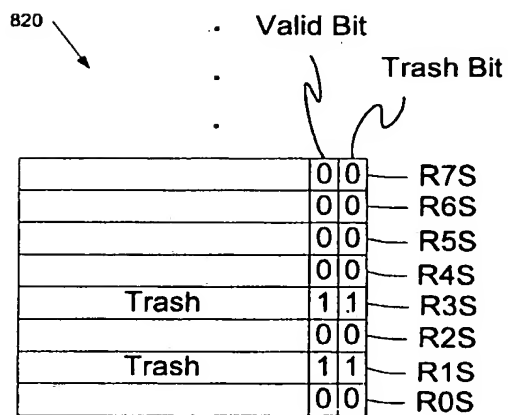


Figure 9A

Figure 9B

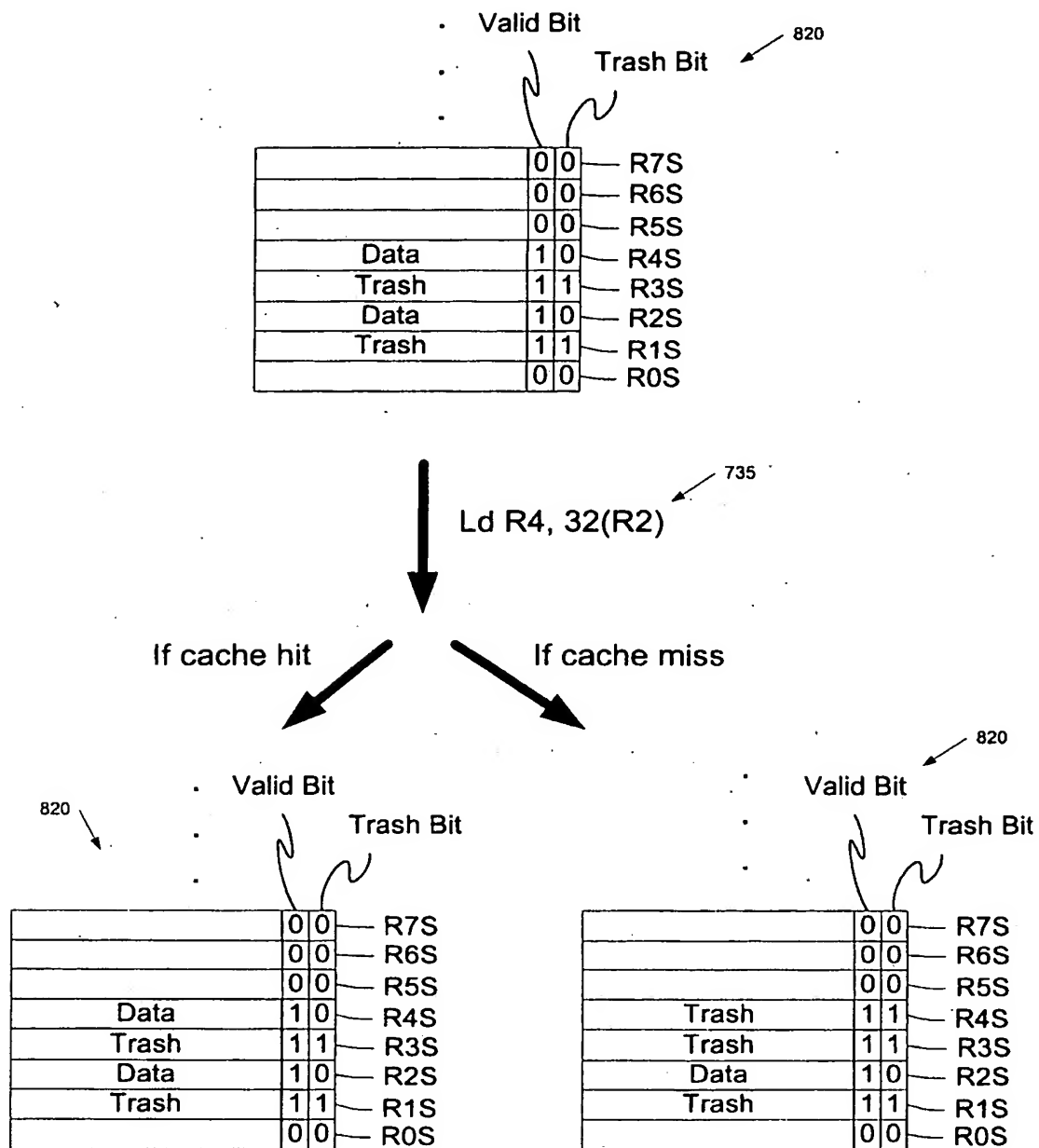
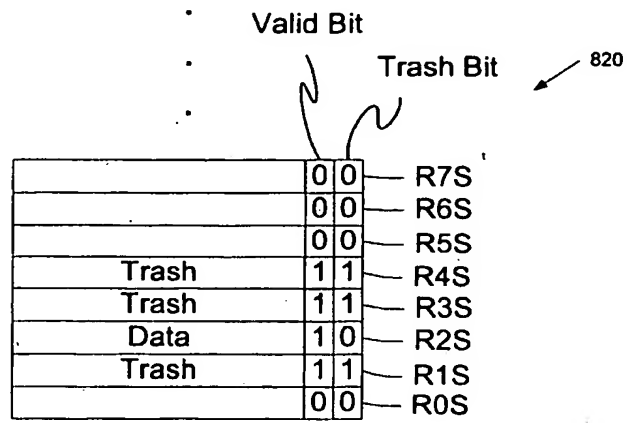


Figure 10



740
Ld R5, 0(R4)

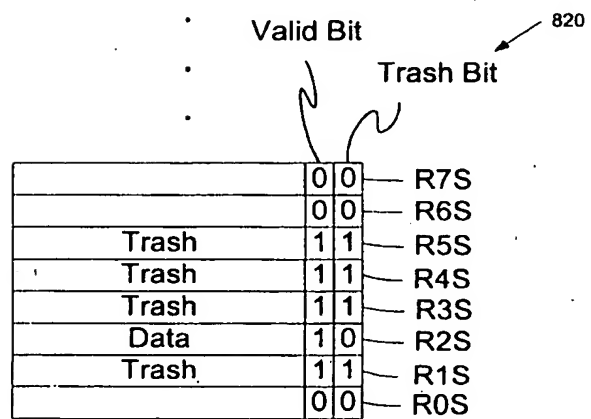


Figure 11

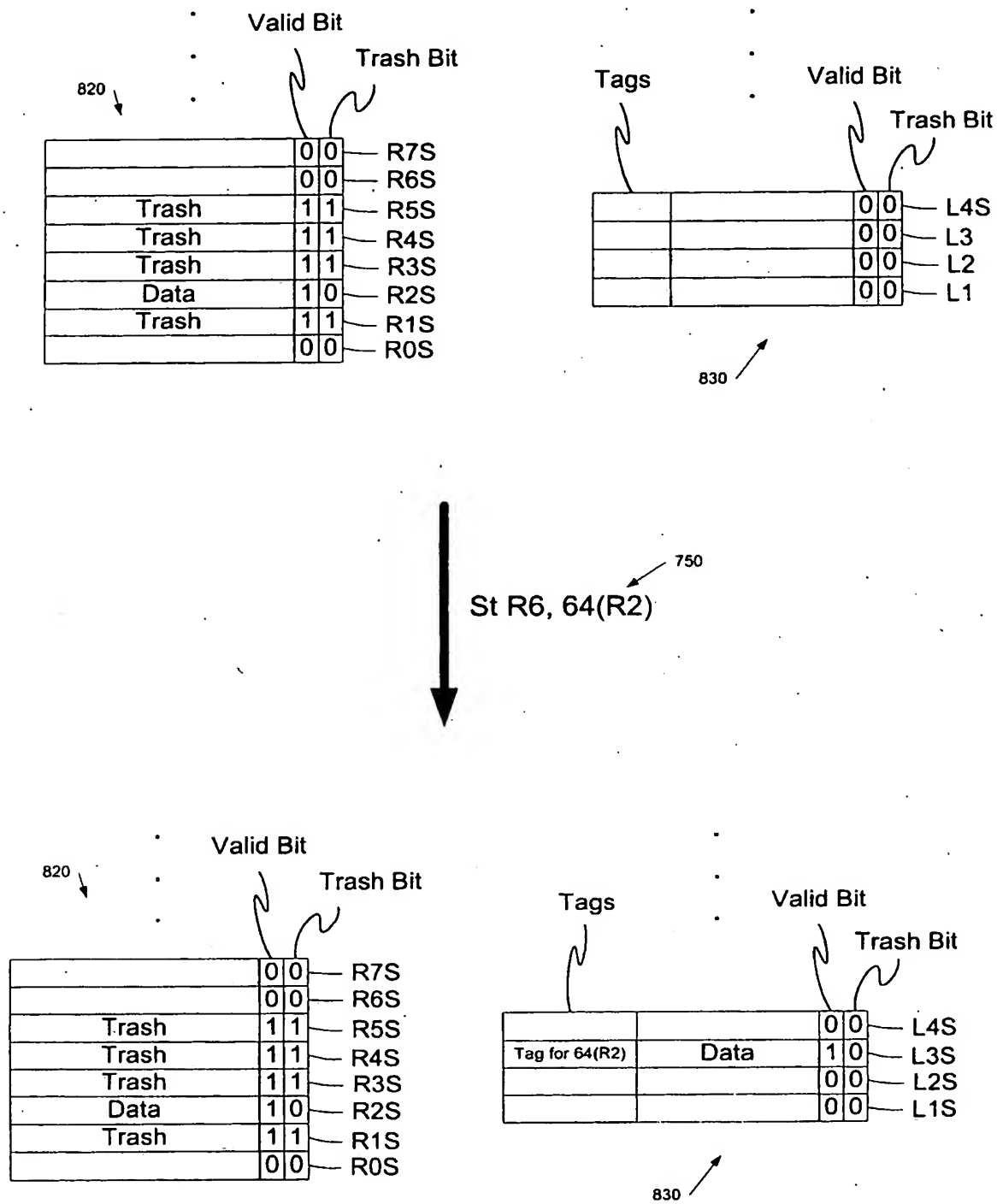
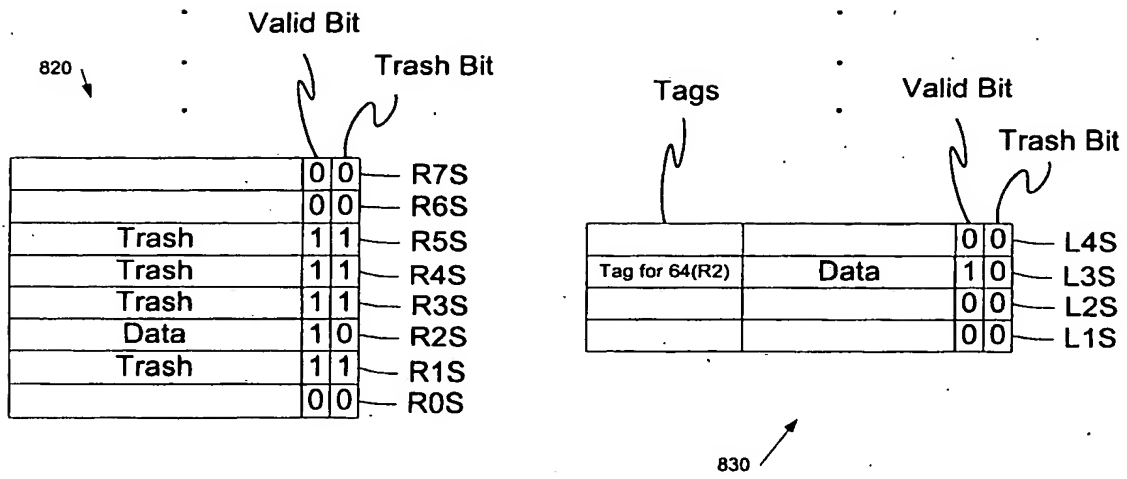


Figure 12



760

St R6, 0(R4)

No - op

Figure 13

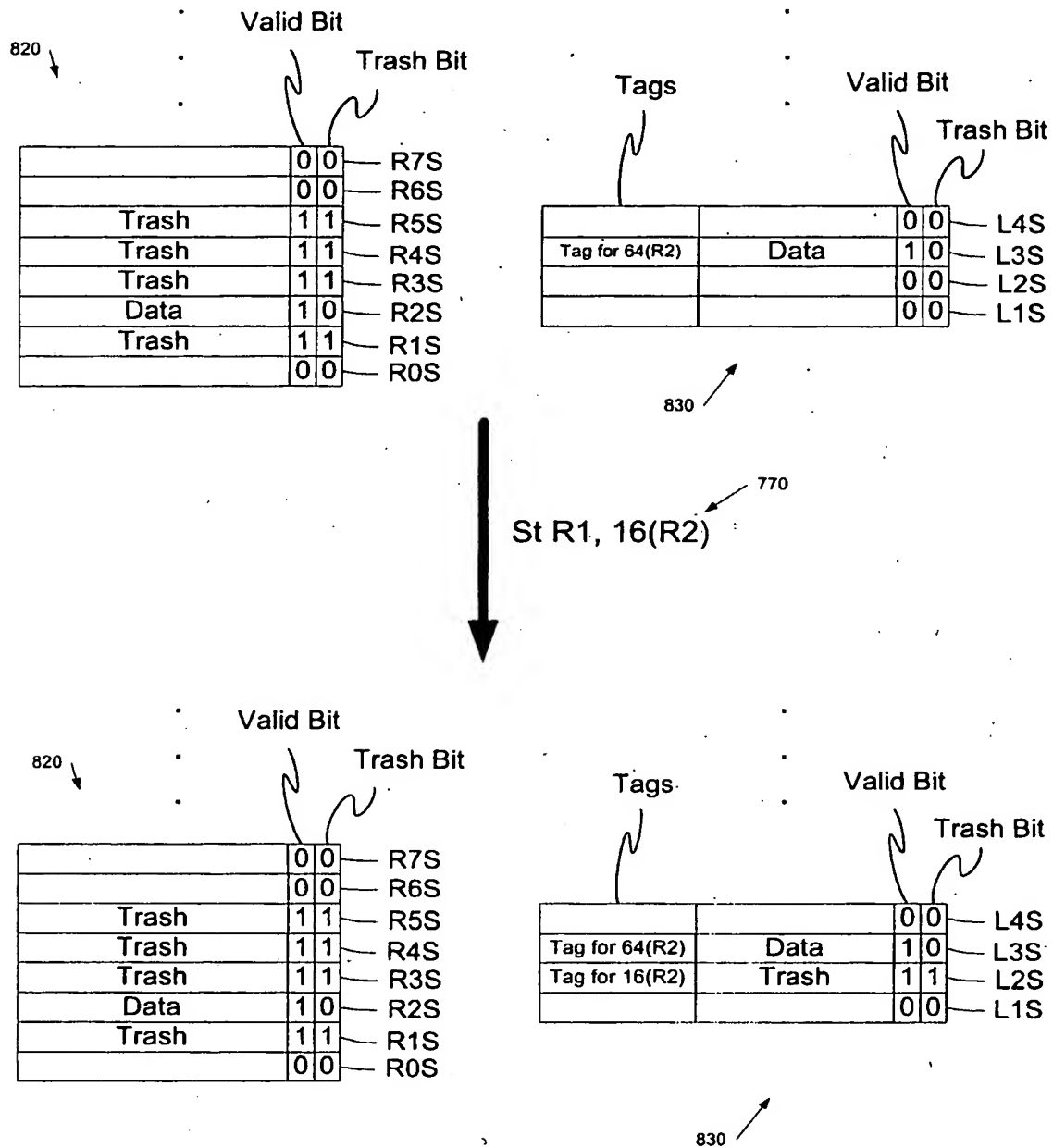


Figure 14

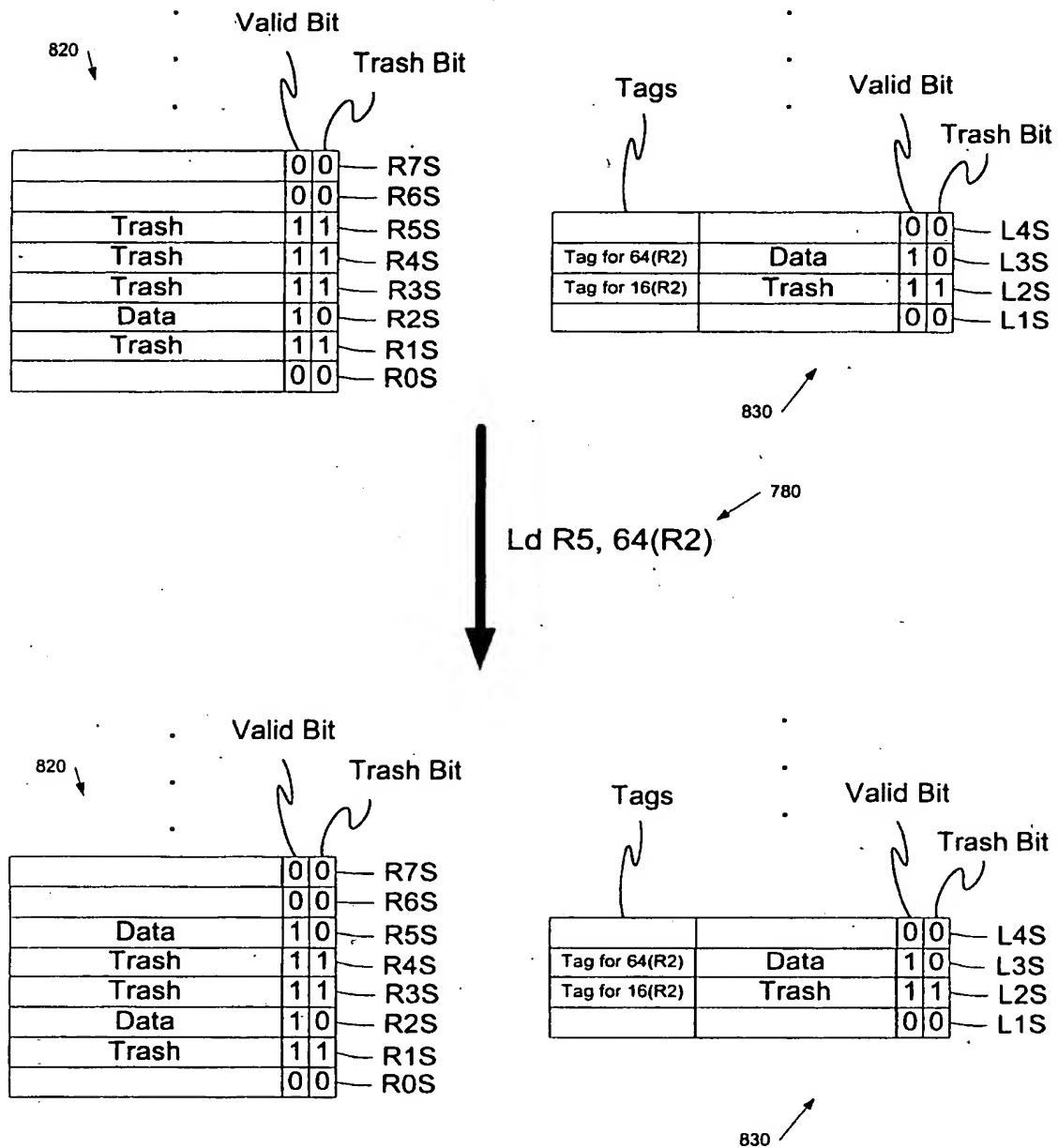


Figure 15

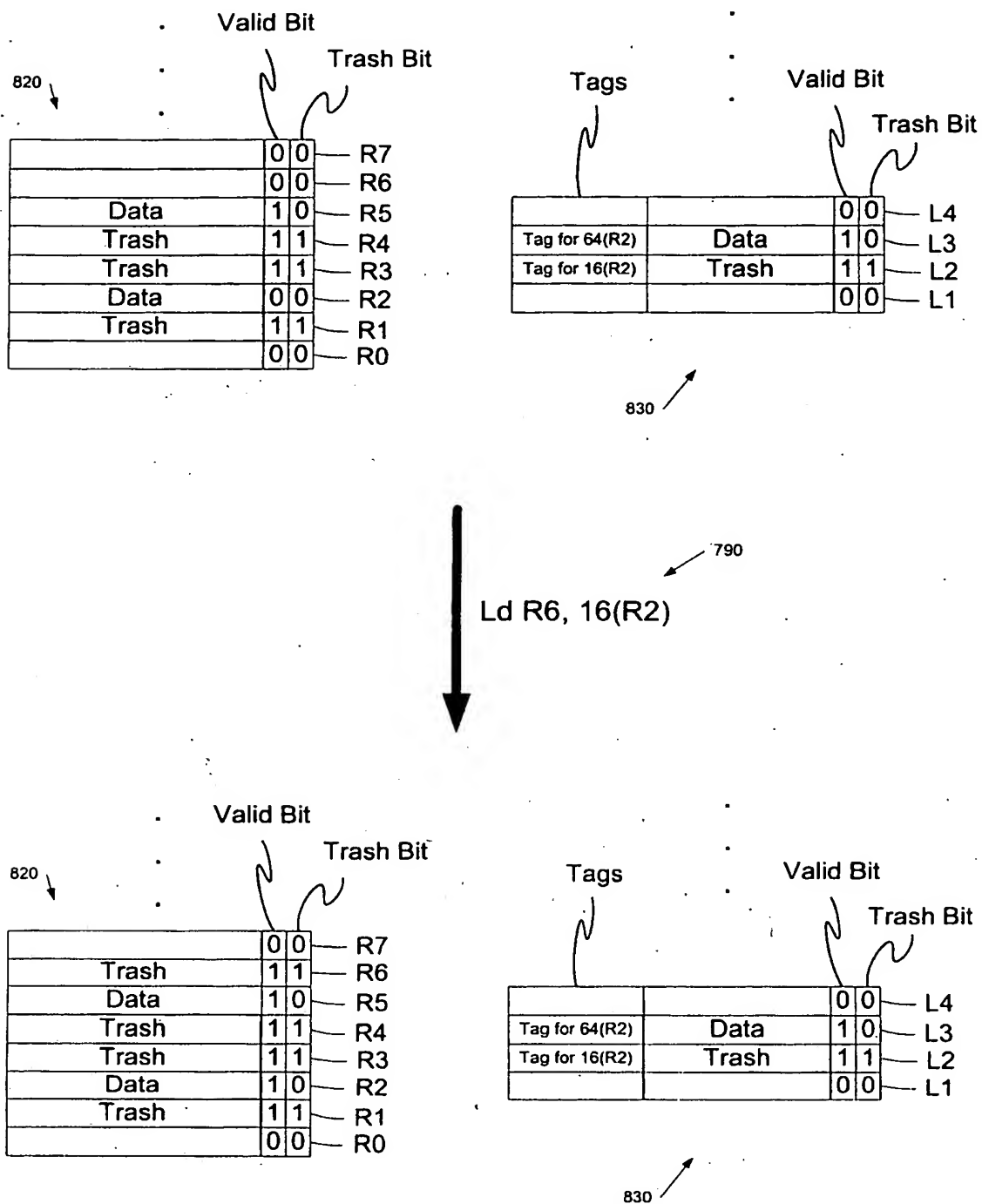


Figure 16